

AMENDMENTS TO SPECIFICATION

Please amend page 6, line 24 to page 7, line 9 in accordance with the following particulars:

FIG. 2 is a schematic diagram of the level shift circuit 2 of FIG. 1 in accordance with the invention. In FIG. 2, the level shift circuit 2 comprises a first switch unit 261, a second switch unit 262, a third switch unit 263, a fourth switch unit ~~264~~264 and a fifth switch unit ~~265~~265. The first switch unit 261 has two N-type thin film transistors (TFTs) 2611, 2612. The second switch unit 262 has two P-type TFTs 2621, 2622. The third switch unit 263 has one P-type TFT 2631 and one N-type TFT 2632. The fourth switch unit 264 has two P-type TFTs 2641, 2642. The fifth switch unit 265 has two N-type TFTs 2651, 2652.